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» Key

IEEE JNL IEEE Journal or Magazine

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Pomeranz, I.; Reddy, S.M.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction](#)
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Digital Object Identifier 10.1109/43.908476
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- ☐ **79. Vector-restoration-based static compaction using random initial omissio**
Pomeranz, I.; Reddy, S.M.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction](#)
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[Instrumentation & Measurement Magazine, IEEE](#)